

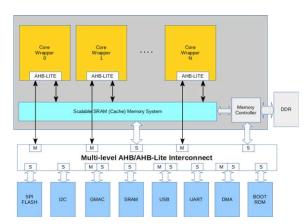
## **Client Project Examples**

## Team Augmentation - Multi ARM Processor SoC Server chip verification

- XtremeEDA team of 25+ engineers worked on multi-cultural and geographically extended complex chip environments
- Multiple areas of expertise applied to block and system level verification
- Worked independently or as members of customer team
- Multiple sized teams provide combined company knowledge for customers to have a true "consulting" experience
- Weekly management to management interaction for risk mitigation



## **Outsourced Development - Reconfigurable ARM Platform Development**



- Platform targets SOCs or FPGAs
- Created complete RTL design and UVM DV environment
- Configurable number and type of multi-processors
- Configurable memory types and peripherals
- Configurable for low power / performance
- AMBA/AHB/AXI/APB, SPI, GPIO, USB, UART, cache subsystems

## Turnkey Development - SV-UVM DV Environment for Network Interface Controller

- Development of VIP and complete testbench environment
- Developed all test cases
- Delivered DV environment and test specifications
- Delivered code and functional coverage to 100%

