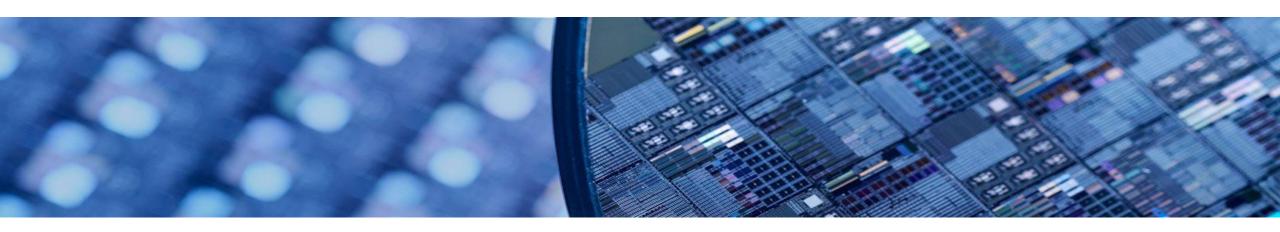


IC'ALPS At the Heart of Your Innovation



IC'Alps at a glance

Our Profile

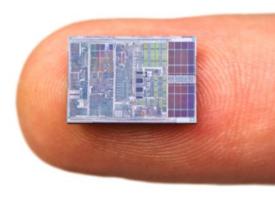
- > A fabless semiconductor company, specialized micro/nano-electronic technology
- > Founded in March 2018, as a subsidiary of DOLIAM group
- > Fast growing team of 30+ passionate experts, 300+ years of cumulated experience
- > ISO13485, EN9100, ISO9001 certifications on going

Our Offer

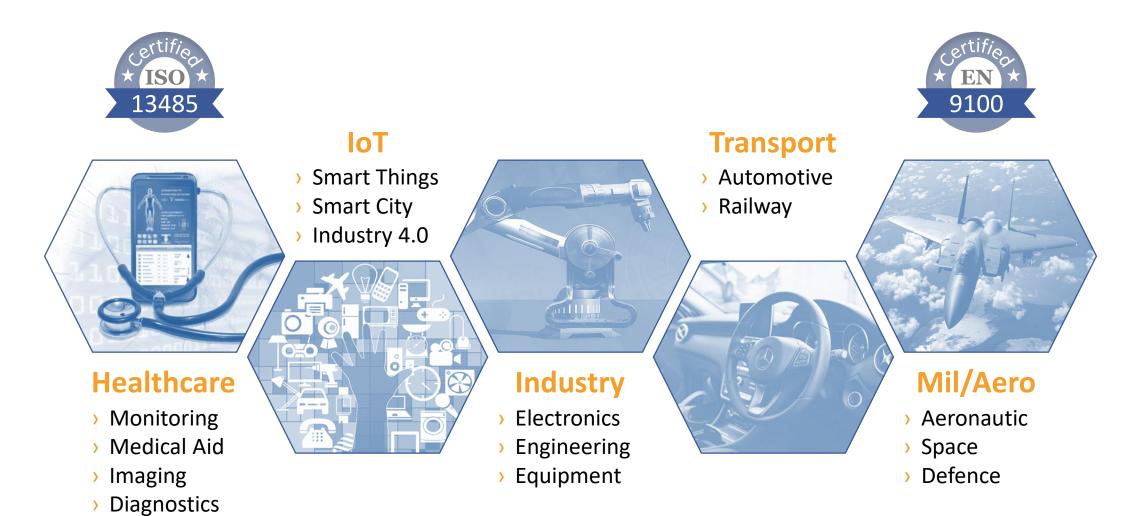
- Application Specific Integrated Circuit (ASIC) design and supply
- > Full turnkey solution or custom block implementation
- Analog, mixed and digital design technology expertise
- From 0.35μm down to 22nm technology nodes
 - » Low power, SOI, high voltage, eFlash... process options support

Our Ecosystem

- > At the heart of the French silicon valley (Grenoble)
- > Partnering with worldwide tier 1 semiconductor foundries and IP providers
- ARM certified design partnership



IC'Alps Market Target



Why an ASIC?



Why IC'Alps as your ASIC partner?



IC'Alps ASIC Design and Supply Process – A la carte

01

FEASIBILITY

- Preliminary architecture
- > Performance analysis
- > Technology selection
- > Supply chain definition
- > IP sourcing strategy



Project commitments
Ready for design

02

DESIGN

- > Final architecture
- > IP sourcing or design
- > Functional design
- > Physical implementation
- > Functional verification



ASIC design schematics Ready for manufacturing

03

PROTOTYPING

- > Foundry manufacturing
- › Package manufacturing
- Component Assembly
- › Initial characterization
- > Functional acceptance



ASIC prototype
Ready for application

04

INDUSTRIALIZATION

- > Final characterization
- > Qualification testing
- Aging testing
- Industrial test setup
- Supply chain setup



ASIC pilot lots Ready for production 05

PRODUCTION

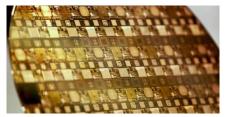
- Production ramp-up
- > First Article Inspection
- Life cycle management



ASIC production lots Production follow up













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