

SignOff
semiconductors

Company behind Quality

SignOff Semiconductors

SignOff started its journey in 2016 as a Design services company and since then we have been developing cutting edge ASIC's that are used in various Automotive, mobile, IoT and communication based SoC applications.

Our core expertise includes Physical Design, Verification and Custom Layout across different foundries & nodes (180Nm – 7Nm), along with providing custom silicon solutions using both in-house expertise and third-party Ips.

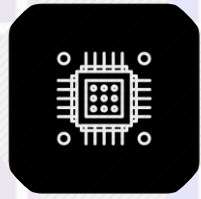
Team having cumulative experience 100+ years in Physical Design and taping out 60+ ASICs including High speed SerDes Ip's, Graphics Cores, Modems, Servers, Storage and RISC Cores targeted to leading foundries in various process nodes including 7nm.

Our Services

- **PHYSICAL DESIGN**
- **STA & SYNTHESIS**
- **FLOW DEVELOPMENT**
- **TURNKEY ASIC SOLUTIONS**
- **ANALOG DESIGN & LAYOUT**
- **RTL DESIGNING AND VERIFICATION**
- **EMBEDDED SYSTEMS**
- **IOT & M2M**

Signof
semiconductor

Our domain specific expertise



SEMICONDUCTORS



AUTOMOTIVE



ARM ASICs



SERVER & NETWORKS



IoT



RISC - V



MEMORY & STORAGE



CLIENT & COMPUTING



CONS. ELECTRONICS

Core service areas

ASIC Frontend Design Services

Signoff has capabilities in house to help customers on the ASIC and FPGA designs in the areas of AI, ML, Edge IOT processors, General purpose processors etc. Team has vast experience covering RTL design, verification, synthesis, timing/power constraint development, budgeting, pre-layout qualification with timing and other sanity checks, Formal verification Synthesis Flow development, FPGA prototyping etc.

ASIC Backend Design Services

Signoff has completed physical design projects ranging from 180nm to TSMC7nm, GF 14nm LPE for its customers. The activities included Fullchip activities like Floorplan, IO planning, Partitioning, CTS Block level Place and Route, Fullchip Place and Route. Timing analysis. Signoff activities include fullchip timing, IR, Electromigration, Physical verification DRC, LVS DFM Formal verification. We can take end to end projects including MPW runs.

IP Services

SignOff provides complete custom silicon solutions using in-house digital/ mixed-signal expertise along with 3rd party IP vendors.

IoT Systems



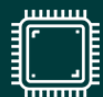
Internet of Things

IoT - Things

Components

- Sensors
- Local Processing
- Communication

SignOff design ASICs which consume Low power, are secure and provide peer to peer connectivity



Semiconductor

Gateway

Components

- Router
- Switch
- Load balancer

SignOff ASIC's are equipped to handle multi protocols and are secure



Systems

Cloud- Internet

Components

- Servers
- Storage
- Service Platforms

ASIC's for public / private servers, secures and providing Analytic support



OUR SERVICES

PHYSICAL DESIGN

TURNKEY ASIC SOLUTIONS

ANALOG DESIGN & LAYOUT

RTL DESIGNING & VERIFICATION

EMBEDDED SYSTEMS & IoT

IP SERVICES



ENGAGEMENT MODELS

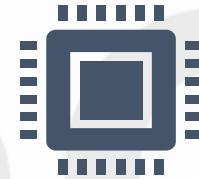
TURNKEY

ODC

RESOURCE AUGMENTATION

PURE TURNKEY

ONSITE



DOMAINS & VERTICALS

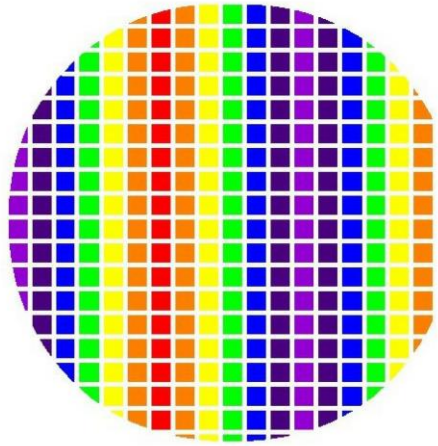
SEMICONDUCTORS

IOT & AUTOMOTIVES

MED. DEVICES & SMART HOMES

MEMORY & STORAGE

SERVER & NETWORKS



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