



ADC 18-BIT 5MS/s – AIP-ADC18B5M5VSAR_CORE

FEATURES

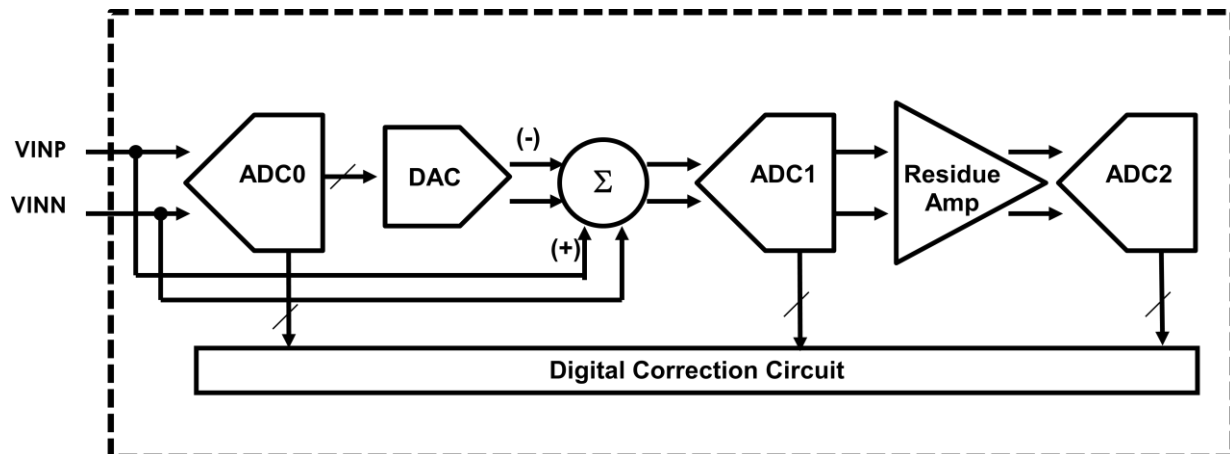
- ▶ Supply voltage: 5.0V
- ▶ Resolution: 18-bit
- ▶ Conversion rate: 5MS/s
- ▶ Architecture: Successive Approximation
- ▶ Input range: $\pm 4.096V$
- ▶ INL and DNL: $< 2LSB$ and $< 2LSB$
- ▶ Latency: $\sim 200ns$
- ▶ Normal and power-down control
- ▶ Power-down mode (consumption $< 0.1\mu A$)
- ▶ Current consumption: NDA required
- ▶ Total core area: NDA required
- ▶ Temperature: $-40C$ to $85C$
- ▶ Process technology: 180nm CMOS with MoM

OVERVIEW

- ▶ A 18-bit successive approximation ADC in a CMOS process.
- ▶ Fully integrated with clock generator, reference, regulators.
- ▶ Normal and power down modes.
- ▶ Ideal for data acquisition, medical applications, diagnostic, motor controller and various sensing applications.

IP STATUS

- ▶ SILICON VERIFIED



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