



# ADC 1.8V 14-BIT, Low Power @ 550nA, 1.6K-2MS/s

## FEATURES

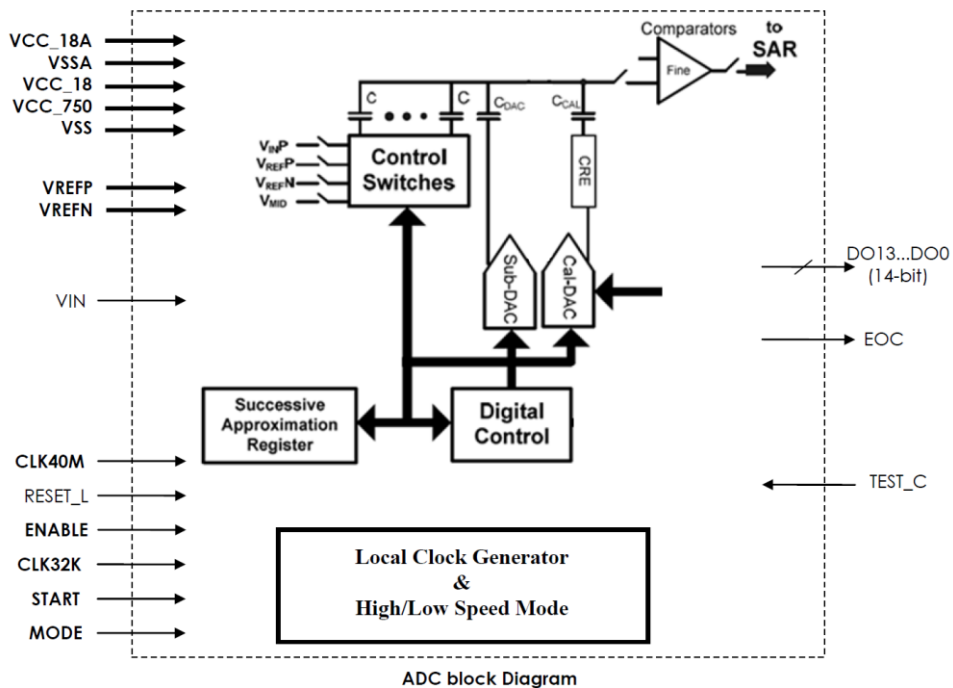
- ▶ Supply voltage: 1.8V
- ▶ Resolution: 14-bit
- ▶ Operating current consumption:
  - 550nA @ 1.6KS/s
  - 135uA @ 2MS/s
- ▶ Power-down consumption <50nA
- ▶ Conversion rate: 1.6KS/s to 2MS/s
- ▶ Input range: 1.8V
- ▶ Architecture: Low-Power SAR
- ▶ INL: < ±4LSB, DNL: < ±1LSB
- ▶ Temperature: -40C to 85C
- ▶ Process technology: 55nm CMOS
- ▶ IP size: NDA required

## OVERVIEW

- ▶ A high resolution low-power 14-bit SAR ADC in CMOS process.
- ▶ Fully integrated with control and internal reference generators.
- ▶ Stand by and power down modes.
- ▶ Ideal for IoT data acquisition, medical, sensor interface and energy harvesting ultra-low-power portable or battery-less applications.

## IP STATUS

- ▶ SILICON VERIFIED



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